

### REMARKS

In response to the action of November 15, 2005, applicants asks that all claims be allowed in view of the amendment to the claims and the following remarks.

Claims 3-8, 11-14 and 16-33 are now pending, of which claims 3, 6, 11-14, 16, 19 and 26 are independent. Claims 4, 5, 7, 8 and 11-14 have been amended, claims 1, 2, 9, 10 and 15 have been cancelled, and claims 16-33 have been added in this amendment. Support for these amendments may be found in the application at, for example, FIG. 3A. No new matter has been introduced.

Claims 3, 5, 6, 8 and 11-14 have been rejected as being anticipated by Stewart (U.S. Patent No. 5,952,789). Applicant requests reconsideration and withdrawal of the rejection because Stewart does not describe or suggest the subject matter of the independent claims, as described more fully below.

Claim 3 recites a current source circuit that includes, among other elements, a first transistor, a second transistor, and a capacitor element connected to the gate electrodes of the first transistor and the second transistor. The circuit also includes a power source line connected to one end of the capacitor element and a current source line connected to the other end of the capacitor element.

Stewart, by contrast, discloses a switching power line (which is said to correspond to the claimed power source line) that is connected to a capacitor and a current source that is connected to transistors T1 and T2, as shown in FIG. 5. Notably, the current source is not connected to the capacitor. As such, Stewart does not describe or suggest a current source line connected to the other end of the capacitor element, as recited in claim 3.

Accordingly, for at least these reasons, applicant requests reconsideration and withdrawal of the rejection of claim 3 and its dependent claim 5.

Similarly to claim 3, claim 6 also recites a current source circuit that includes, among other elements, a first transistor, a second transistor, a third transistor, a capacitor element connected to the gate electrodes of the first and second transistors, a power source line connected to one end of the capacitor element and a current source line connected to the other end of the capacitor element. Accordingly, at least the reasons discussed above with respect to claim 3, applicant requests reconsideration and withdrawal of the rejection of claim 6 and its dependent

claim 8.

Claim 11 recites a method for driving a current source circuit that includes, *inter alia*, feeding current supplied from a power source line to a current source line through first and second transistors that are connected in parallel. By contrast, Stewart discloses, in FIG. 5, transistors T1 and T2 that are connected in series. As such, Stewart does not describe or suggest feeding current supplied from the power source line to the current source line through first and second transistors that are connected in parallel, as recited in claim 11. For at least these reasons, applicant requests reconsideration and withdrawal of the rejection of claim 11.

Claim 12 recites a method for driving a current source circuit having a first transistor, a second transistor and a capacitor element connected to the gate electrodes of the first transistor and the second transistor. The circuit also includes a current source line and a power source line connected to the capacitor element. The method includes connecting the first and second transistors in parallel when a setting operation is performed on the transistors, and in series when current is supplied from the first and second transistors to an object to be driven.

As described above, Stewart does not describe or suggest the circuit as recited in claim 12 in which the transistors are connected in parallel, and, accordingly, does not describe or suggest connecting the transistors in parallel when a setting operation is performed, as recited in claim 12. For at least these reasons, applicant requests reconsideration and withdrawal of the rejection of claim 12.

Claim 13 recites a method for driving a current source circuit having a first transistor, a second transistor, a capacitor element connected to the gate electrodes of the first and second transistors. The circuit also includes a current source line and a power source line that are connected to the capacitor element. The method includes, *inter alia*, supplying current based on the predetermined amount of voltage to the first and second transistors, which are connected in parallel, such that the transistors can feed a predetermined amount of current.

In contrast, and as noted above, Stewart does not describe or suggest the circuit in which the transistors are connected in parallel, and, accordingly, does not describe or suggest supplying current based on the predetermined amount of voltage to the first and second transistors, which are connected in parallel, such that the transistors can feed a predetermined amount of current, as recited in claim 13. For at least these reasons, applicant requests reconsideration and withdrawal

of the rejection of claim 13.

Claim 14 recites a method for operating a display device including a current source circuit having, like the circuit of claim 13, a first transistor, a second transistor, a capacitor element connected to the gate electrodes of the first and second transistors, and a current source line and a power source line that are connected to the capacitor element. Like claim 13, the method of claim 14 includes, *inter alia*, supplying current based on the predetermined amount of voltage to the first and second transistors, which are connected in parallel, such that the transistors can feed a predetermined amount of current. Accordingly, for at least the reasons described above with respect to claim 13, applicant requests reconsideration and withdrawal of the rejection of claim 14.

Claims 4 and 7, which depend, respectively, from claims 3 and 6, have been rejected as being unpatentable over Stewart in view of Yamagishi (U.S. Patent No. 6,501,466). Yamagishi, which is cited in the action for disclosing a current source system having transistors of a P-channel type, does not remedy the failure of Stewart to describe or suggest the subject matter of the independent claims. For at least these reasons, applicant respectfully requests withdrawal of the rejections of claims 4 and 7.

New independent claims 16, 19, 22 and 26 each recite a current source circuit including a power source line connected to one end of the capacitor element; and a current source line connected to the other end of the capacitor element. Accordingly, for at least the reasons noted above with respect to claim 3, applicant submits that claims 16, 19, 22 and 26 are allowable, as are their respective dependent claims 17, 18, 20, 23-25 and 27-29.

It is believed that all of the pending issues have been addressed. However, the absence of a reply to a specific rejection, issue or comment does not signify agreement with or concession of that rejection, issue or comment. In addition, because the arguments made above may not be exhaustive, there may be reasons for patentability of any or all pending claims (or other claims) that have not been expressed. Finally, nothing in this reply should be construed as an intent to concede any issue with regard to any claim, except as specifically stated in this reply, and the amendment of any claim does not necessarily signify concession of unpatentability of the claim prior to its amendment.

Applicant submits that all claims are in condition for allowance.

Applicant : Hajime Kimura et al.  
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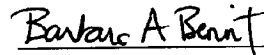
Attorney's Docket No.: 12732-162001 / US6582

Pursuant to 37 CFR §1.136, applicant hereby petitions that the period for response to the action dated November 15, 2005, be extended for one month to and including March 15, 2006.

The fee in the amount of \$770.00 in payment of the for excess claim fees (\$650.00) and for the Petition for Extension of Time fee (\$120.00) is being paid concurrently herewith on the Electronic Filing System (EFS) by way of Deposit Account authorization. Please apply any other charges or credits to deposit account 06-1050.

Respectfully submitted,

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